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**Substitute for form 1449A/PTO**

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

*(Use as many sheets as necessary)*

Sheet

1

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2

**Complete if Known**

Application Number	10/722,808
Filing Date	November 26, 2003
First Named Inventor	Gregory P. Dittmer
Art Unit	2838
Examiner Name	Jeffrey L. Sterrett
Attorney Docket Number	LT-167

## U. S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

**Examiner  
Signature**

Sterrett

Date  
Considered

2/6/06

**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional). 2 See Kinds Codes of USPTO Patent documents at [www.uspto.gov](http://www.uspto.gov) or WIPO ST.16. 3 Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). 4 For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. 5 Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. 6 Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>				<i>Application Number</i>	10/722,808
<i>(Use as many sheets as necessary)</i>				<i>Filing Date</i>	November 26, 2003
Sheet	2	of	2	<i>First Named Inventor</i>	Gregory P. Dittmer
				<i>Art Unit</i>	2838
				<i>Examiner Name</i>	Jeffrey L. Sterrett
				<i>Attorney Docket Number</i>	LT-167

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
QLD		Linear Technology, Nelson., C., "The LT1432:5 Volt Regulator Achieves 90% Efficiency," Linear Technology Magazine, Vol. 2, No. 1, p 12, continued on p 19, 2/92			
↑		Maxim Integrated Products, Inc., "MAX635/36/37 Fixed Output CMOS Inverting Switching Regulators," Maxim 1989 Integrated Circuits Data Book, pp. 6-49 to 6-56, 1989			
		Maxim Integrated Products, Inc., "MAX639 High-Efficiency, +5V Adjustable Step-Down Switching Regulator," Datasheet, 12/91			
		Maxim Integrated Products, Inc., "MAX783 Triple-Output Power-Supply Controller for Notebook Computers," Datasheet, 05/94			
		Micro Linear Corporation, "ML4873 Battery Power Control IC," Advance Information Data Sheet, March 15, 1993, pp. 1-8.			
		Uchida, Takahito, "Control Circuit for Switching Regulator," Japanese Inventor Associated Disclosed Technology Publication No. 92-2362, published 2/15/92 (in Japanese, with translation)			
		Unitrode, "UCC29421/2, UCC39421/2 Multimode High Frequency PWM Controller," Preliminary Datasheet, 10/1999			
		Wilcox, M., "The LT1158: Low Voltage, N-Channel Bridge Design Made Easy," Linear Technology Magazine, Vol. 2, No. 1, 2/92			
↓		Williams, J., "Basic Principles and Ingenious Circuits Yield Stout Switchers," EDN, 01/18/90			
QLD		Williams, J., "Refine V/F-converter operation with novel design techniques," EDN, 05/30/85			

Examiner Signature	Sterrett	Date Considered	2/6/06
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